

Substitute for form 1448A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Complete if Known

Application Number	10/618,168
Filing Date	July 11, 2003
First Named Inventor	Alan Kyker
Art Unit	2121 2187
Examiner Name	Unassigned
Attorney Docket Number	42P7938C

Sheet	1	of	2
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U.S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

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**Examiner
Signature**

Date Considered

11,22,05

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*Applicant's unique citation designation number (optional). *See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 801.04. *Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). *For Japanese patent documents, the indication of the year of reign of the Emperor must precede the serial number of the patent document. *Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. *Applicant is to place a check mark here if English language translation is attached.

Based on FTO/SB/06A (08-03) as modified by Blakey, Solotoff, Taylor & Zalman (wtr) 08/11/2003.

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NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No.†	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T*
N.H.		EUGENE R. HNATEK; Random-Access Memories and Content-Addressable Memories, A User's Handbook of Semiconductor Memories; 1977; pgs. 591-608; Wiley-Interscience	
		WILLIAM STALLINGS; 7.3 memory Management, Computer Organization and Architecture, Designing for Performance, 4th Edition; 1996; pgs. 240-263; Prentice Hall,	
		HAMACHER, VRANESIC & ZAKY; Computer Organization, 2nd Edition; 1984; pgs. 306-329; McGraw-Hill Book Company.	
		PATTERSON & HENNESSY; Computer Architecture: A Quantitative Approach; 1990; pgs 408-475; Morgan Kaufmann Publishers, San Mateo, CA.	
		RICHARD KAIN; Advanced Computer Architecture: A System Design Approach; 1996; pgs. 75-88 and 456-474; Prentice Hall, Englewood Cliffs, New Jersey.	
		LEAN-LOUP BAAER; Computer Systems Architecture; 1980; pgs. 139-166 and 314-325; Computer Science Press, Rockville, MD.	

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